

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	126	(mother adj board) and simulation	USPAT	OR	OFF	2005/01/28 14:31
L2	3	1 and (simulation or simulator).ti.	USPAT	OR	OFF	2005/01/28 14:28
L3	0	1 and (north adj bridge adj chip)	USPAT	OR	OFF	2005/01/28 14:31
L4	12	1 and (chip adj set)	USPAT	OR	OFF	2005/01/28 14:33
L5	74	(circuit adj board) and simulation. ti.	USPAT	OR	OFF	2005/01/28 14:34
L6	91	(circuit adj board) and simulation. ti.	USPAT	OR	ON	2005/01/28 14:34
L7	35	6 and (software same model)	USPAT	OR	ON	2005/01/28 15:23
L8	1	simulat\$6 and indent\$5 and creat\$3 and (programming adj language) and (function adj call\$2) and virtual and link\$4 and (cycle adj accurate) and transaction	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/28 15:26
L9	14	("20010003844" "20030028690" "4241418" "5659484" "5778237" "5802290" "5996083" "6308311" "6345384" "6412061" "6560665" "6567837" "6587385").PN. OR ("6691301"). URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/01/28 15:32
L10	2	((("5748875") or ("6154719")).PN.	USPAT	OR	OFF	2005/01/28 15:33
L11	0	10 and (cycle adj accurate adj information)	USPAT	OR	ON	2005/01/28 15:33
L12	0	(cycle adj accurate adj information)	USPAT	OR	ON	2005/01/28 15:34
L13	182	(cycle adj accurate)	USPAT	OR	ON	2005/01/28 15:34
L14	0	10 and (cycle adj accurate)	USPAT	OR	ON	2005/01/28 15:34
L15	2	13 and (circuit adj model)	USPAT	OR	ON	2005/01/28 15:34